



## Job Description

**Job title:** Senior Design Engineer

**Location:** SLP

**Department:** Research and Development

**Reports to:** Principal Design Engineer – R&D Team

## Purpose of Job

The role is within the R&D Team where the focus is to design and develop technologies which can support the future direction of the business groups.

The primary function of the role is hardware design and in particular FPGA development using VHDL. The position requires a practical technical background in a design & development role, also a degree of flexibility and a good understanding of the importance of systematic analysis, thorough documentation and methodical work practices.

The members of the R&D Team work together to define architecture and solutions, so the candidate is expected to be self motivated, self reliant and pro-active in maintaining communication with other members of the team and the team leader.

Technologies produced by the R&D Team require combined hardware and software solutions. Therefore the ability to create low level device drivers on embedded and/or PC platforms is advantageous.

## Main Functions and Responsibilities

- Contribute to hardware architecture and design
- Participate in project proposals including timescale, cost and resource estimates
- Produce reports, project and design documentation related to the project
- Maintain awareness of technology likely to impact Broadcast market
- Ensure any IP developed is appropriately protected

### Other Responsibilities

- Keep up to date with technical developments, hardware design tools and techniques
- Follow company standard practice and procedures

## Qualifications / Knowledge / Experience

### Education/Certification

- Experience in hardware design, ideally within the Broadcast industry, supported with a relevant technical qualification to degree standard.

### Knowledge/Experience in the following areas is essential:

- Proficient in VHDL design and simulation
  - Experience of high end Xilinx and/or Altera FPGA devices
  - Experience of embedded micro controllers
  - Experience of PCB design and debug
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Experience in the following areas is desirable:

- Familiarity with video fundamentals, video/Audio standards and formats
- Familiarity with a wide range of IT technologies e.g. Ethernet, PCIe and USB
- Methodical approach towards following design processes and procedures
- Familiarity with PC hardware & software
- Experience of low level software development in an embedded and/or PC environment is beneficial
- ModelSim, MATLAB, Altera Quartus, Xilinx ISE, Cadence OrCAD, ViewDraw, Ruby, Subversion source control and JIRA defect management tools

Skills/Abilities

- Good Interpersonal and communication skills
- Must demonstrate ability to exercise independent judgement, initiative and tact in both internal and external relationships
- Problem solving aptitude
- Confidentiality/trust
- Accurate and attentive to detail
- Ability to work unsupervised and as a team player when required

## Contacts

### Internal

- Algorithms Group
- Product Development

### External

- Some contact with third-party development partners and customers
- 3rd Party Library vendors

## Additional Information

- This role may require occasional travel within the UK and internationally to Snell and client offices.

## Performance Standards

- Achieve mutually agreed objectives, standards and dates.
- Performance reviews will be conducted on an annual basis in addition to interim progress reviews.

To apply please forward your CV, covering note and salary details to [Laura.Powell@snellgroup.com](mailto:Laura.Powell@snellgroup.com)

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